



ABSTRACT

A method and a circuit for controlling the access to all or part of the content of a memory that is integrated with a microprocessor, a priority-holding interrupt, at least one register of keys, and at least one access control algorithm contained in a second auxiliary memory are used. The content of at least one also integrated storage element and the content of the key register, the content of the auxiliary memory being programmable only once.

RECEIVED

AUG 11 2004

Technology Center 2100